L Number	Hits	Search Text	DB	Time stamp
1	214	(712/24).CCLS.	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/06/14 07:55
2	65	(712/21).CCLS.	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/06/14 07:55
3	171	(712/206).CCLS.	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/06/14 07:55
4	275	(712/209).CCLS.	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/06/14 07:55
5	379	(712/210).CCLS.	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/06/14 07:55
6	431	(712/215).CCLS.	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/06/14 07:55
-	0	VLIW same (emulation adj1 register)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/09/05 14:25
-	64	VLIW same (instruction adj1 register)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/09/05 14:33
-	13	VLIW same (instruction adj1 register) same pipeline	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/09/05 14:32
-	0	emulation adj1 instruction adj1 register	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/09/05 14:32
-	21	emulation near4 instruction adj1 register	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/09/05 14:32
-	21	emulation near4 (instruction adj1 register)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/09/05 14:32
-	2	VLIW same (instruction adj1 register) same valid$3	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/09/05 14:34
-	19	VLIW same (valid$3 near3 instruction)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/09/05 14:38
-	3	(("5941980") or ("4858105") or ("6065108")).PN.	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/09/05 14:38

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-	0	emulation near4 (width adj1 bit$1)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/06/1C
-	1	emulation with (width adj1 bit$1)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/06/10 17:00
-	0	(valid near4 (width adj1 bit$1)) near4 instruction	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/06/10 16:50
-	12	(width near4 instruction) near4 valid$3	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/06/10 16:52
-	2	(width near4 bit$1) near4 instruction near4 valid$3	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/06/10 16:54
-	0	(width adj1 bit$1) near4 (instruction adj1 valid$3)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/06/10 16:54
-	0	(width adj1 bit$1) same (instruction adj1 valid$3)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/06/10 16:54
-	0	(width adj1 bit$1) with (instruction adj1 valid$3)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/06/10 16:54
-	0	(width adj1 bit$1) with (instruction near4 valid$3)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/06/10 16:55
-	1	(width near2 bit$1) with (instruction near4 valid$3)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/06/10 16:56
-	0	(instruction adj1 width) near4 (instruction adj1 valid$3)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/06/10 16:56
-	0	(instruction adj1 width) same (instruction adj1 valid$3)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/06/10 16:56
-	0	(instruction adj1 width) with (instruction adj1 valid$3)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/06/10 16:57
-	0	(instruction adj1 width) near4 (valid$3)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/06/10 16:57
-	188455	(instruction adj1 width) sa,e (valid$3)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/06/10 16:57
-	3	(instruction adj1 width) same (valid$3)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/06/10 16:59

-	0	(instruction adj1 width) with (valid$3)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/06/10 16:59
-	2	emulation same (width adj1 bit$1)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/06/10 17:01
-	1	VLIW near4 (width near4 bits)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/06/10 17:01
-	4	VLIW with (width near4 bits)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/06/10 17:02
-	4	(variable near4 instruction$1 near4 length$1) near4 (bit$1 near4 width)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/06/10 17:12
-	68	(instruction near4 emulation) near4 register$1	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/06/10 17:12
-	5	(instruction adj1 emulation) near4 register$1	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/06/10 17:17
-	2	(instruction adj1 emulation) near4 (emulation near4 register$1)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/06/10 17:20
-	3	(instruction adj1 emulation) with (emulation near4 register$1)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/06/10 17:27
-	16	(instruction adj1 emulation) same (emulation near4 register$1)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/06/10 17:27